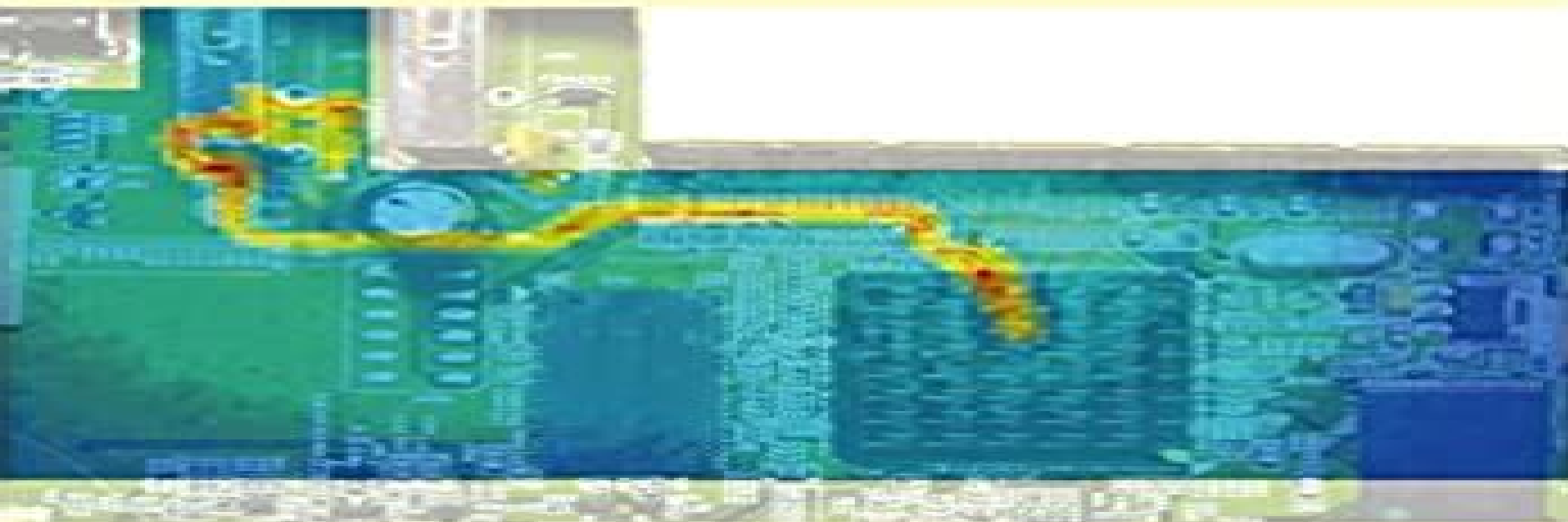# SYSTEM LEVEL ESD CO-DESIGN

Edited by

CHARVAKA DUVVURY
HARALD GOSSNER

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Institute of Electrical and Electronics Engineers

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System Level ESD Co-design ,2015 Demystifies the concept of system level ESD and details its difference from the conventional component level ESD design and testing Describes the protection elements and designs and focuses on the co design an optimization methodology to address both issues in the same design space **Electromagnetic Compatibility for Space Systems Design** Nikolopoulos, Christos D.,2018-03-02 In the aerospace industry avoiding operating issues especially in regard to space missions and satellite structures is crucial The vast majority of these issues can be traced to disturbances in the electromagnetic fields used Electromagnetic Compatibility for Space Systems Design is a critical scholarly resource that examines the applications of electromagnetic compatibility and electromagnetic interference in the space industry Featuring coverage on a wide range of topics such as magnetometers electromagnetic environmental effects and electromagnetic shielding this book is geared toward managers engineers and researchers seeking current research on the applications of electromagnetic technologies in the aerospace field **System Level ESD Co-Design** Charvaka Duvvury,Harald Gossner,2017-05-05 An effective and cost efficient protection of electronic system against ESD stress pulses specified by IEC 61000 4 2 is paramount for any system design This pioneering book presents the collective knowledge of system designers and system testing experts and state of the art techniques for achieving efficient system level ESD protection with minimum impact on the system performance All categories of system failures ranging from hard to soft types are considered to review simulation and tool applications that can be used The principal focus of System Level ESD Co Design is defining and establishing the importance of co design efforts from both IC supplier and system builder perspectives ESD designers often face challenges in meeting customers system level ESD requirements and therefore a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system performance With contributions from Robert Ashton Jeffrey Dunnihoo Micheal Hopkins Pratik Maheshwari David Pomerenke Wolfgang Reinprecht and Matti Usumaki readers benefit from hands on experience and in depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD robust systems that include mobile and automotive applications The first dedicated resource to system level ESD co design this is an essential reference for industry ESD designers system builders IC suppliers and customers and also Original Equipment Manufacturers OEMs Key features Clarifies the concept of system level ESD protection Introduces a co design approach for ESD robust systems Details soft and hard ESD fail mechanisms Detailed protection strategies for both mobile and automotive applications Explains simulation tools and methodology for system level ESD co design and overviews available test methods and standards Highlights economic benefits of system ESD co design **Recent Trends on Electromagnetic Environmental Effects for Aeronautics and Space Applications** Nikolopoulos, Christos D.,2020-11-27 Electromagnetic compatibility and regulatory compliance issues are subjects of great importance in

electronics engineering Avoiding problems regarding an electronic system s operation while always important is especially critical in space missions and satellite structures Many problems can be traced to EM field disturbances as interference from unintended sources and other electromagnetic phenomena As a result stringent requirements are to be met in terms of electromagnetic emissions levels The inclusion of this electromagnetic environment in the design of a multimillion mission can lead to a system that is able to withstand whatever challenge the environment throws at it Failure to do so may lead to important data corruption or loss destruction of expensive instruments waste of resources and even a total mission failure Research in this area focuses on the studying of the applications of electromagnetic compatibility and electromagnetic interference in the space industry Recent Trends on Electromagnetic Environmental Effects for Aeronautics and Space Applications will provide relevant theoretical frameworks and the latest empirical research findings in electromagnetic compatibility and electromagnetic interference EMC EMI for the aerospace industry This book examines all the necessary information for all matters that can possibly affect the system design of a spacecraft and can be a useful reference to space system engineers and more While highlighting topics such as artificial intelligence electromagnetic testing environmental shielding and EMC modelling techniques this book is ideal for professionals spacecraft designers science and data processing managers electrical and mechanical engineers EMC testing engineers and researchers working in the aerospace industry along with practitioners researchers academicians and students looking for necessary information for all the matters that can possibly affect the system design of a spacecraft *System Level ESD Protection* Vladislav Vashchenko,Mirko Scholz,2014-03-21 This book addresses key aspects of analog integrated circuits and systems design related to system level electrostatic discharge ESD protection It is an invaluable reference for anyone developing systems on chip SoC and systems on package SoP integrated with system level ESD protection The book focuses on both the design of semiconductor integrated circuit IC components with embedded on chip system level protection and IC system co design The readers will be enabled to bring the system level ESD protection solutions to the level of integrated circuits thereby reducing or completely eliminating the need for additional discrete components on the printed circuit board PCB and meeting system level ESD requirements The authors take a systematic approach based on IC system ESD protection co design A detailed description of the available IC level ESD testing methods is provided together with a discussion of the correlation between IC level and system level ESD testing methods The IC level ESD protection design is demonstrated with representative case studies which are analyzed with various numerical simulations and ESD testing The overall methodology for IC system ESD co design is presented as a step by step procedure that involves both ESD testing and numerical simulations **System Level ESD Co-Design** Charvaka Duvvury,Harald Gossner,2015-08-04 An effective and cost efficient protection of electronic system against ESD stress pulses specified by IEC 61000 4 2 is paramount for any system design This pioneering book presents the collective knowledge of system designers and system testing experts and state of the art techniques for achieving efficient

system level ESD protection with minimum impact on the system performance All categories of system failures ranging from hard to soft types are considered to review simulation and tool applications that can be used The principal focus of System Level ESD Co Design is defining and establishing the importance of co design efforts from both IC supplier and system builder perspectives ESD designers often face challenges in meeting customers system level ESD requirements and therefore a clear understanding of the techniques presented here will facilitate effective simulation approaches leading to better solutions without compromising system performance With contributions from Robert Ashton Jeffrey Dunnihoo Micheal Hopkins Pratik Maheshwari David Pomerenke Wolfgang Reinprecht and Matti Usumaki readers benefit from hands on experience and in depth knowledge in topics ranging from ESD design and the physics of system ESD phenomena to tools and techniques to address soft failures and strategies to design ESD robust systems that include mobile and automotive applications The first dedicated resource to system level ESD co design this is an essential reference for industry ESD designers system builders IC suppliers and customers and also Original Equipment Manufacturers OEMs Key features Clarifies the concept of system level ESD protection Introduces a co design approach for ESD robust systems Details soft and hard ESD fail mechanisms Detailed protection strategies for both mobile and automotive applications Explains simulation tools and methodology for system level ESD co design and overviews available test methods and standards Highlights economic benefits of system ESD co design   Electrical Overstress/Electrostatic Discharge Symposium Proceedings ,2004

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