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Computer Architecture Gérard Blanchet, Bertrand Dupouy, 2013-01-24 This book lays out the concepts necessary to understand how a computer works For reasons of clarity the authors have deliberately chosen examples that apply to machines from all eras without having to water down the contents of the book This choice helps to show how techniques concepts and performances have evolved since the first computers The book is divided into five parts The first four which are of increasing difficulty are the core of the book Elements of a Basic Architecture Programming Model and Operation Memory Hierarchy Parallelism and Performance Enhancement The final part provides hints and solutions to the exercises in the book as well as appendices The reader may approach each part independently based on their prior knowledge and goals

Optimization of Power Flow Computation Methods Christoph Kattmann, 2022-09-13 Power flow computations are a cornerstone of many simulations regarding the electric grid This thesis evaluates the landscape of power flow computation methods with a focus on practical computational performance in large scale simulations as they occur in modern distribution grid planning The investigation involves various model assumptions different algorithms implementation details and unconventional computational optimization methods As a result the implementations devised in this thesis are up to a thousand times faster for large scale grid simulations than established solutions

Matrix Functions and Matrix Equations Zhaojun Bai, 2015-09-04 Matrix functions and matrix equations are widely used in science engineering and social sciences due to the succinct and insightful way in which they allow problems to be formulated and solutions to be expressed This book covers materials relevant to advanced undergraduate and graduate courses in numerical linear algebra and scientific computing It is also well suited for self study The broad content makes it convenient as a general reference to the subjects

Computer Architecture John L. Hennessy, David A. Patterson, 2011-10-07 Computer Architecture A Quantitative Approach Fifth Edition explores the ways that software and technology in the cloud are accessed by digital media such as cell phones computers tablets and other mobile devices The book which became a part of Intel's 2012 recommended reading list for developers covers the revolution of mobile computing It also highlights the two most important factors in architecture today parallelism and memory hierarchy This fully updated edition is comprised of six chapters that follow a consistent framework explanation of the ideas in each chapter a crosscutting issues section which presents how the concepts covered in one chapter connect with those given in other chapters a putting it all together section that links these concepts by discussing how they are applied in real machine and detailed examples of misunderstandings and architectural traps

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Parallel Programming Thomas Rauber, Gudula Rünger, 2023-04-04 This textbook covers the new development in processor architecture and parallel hardware It provides detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers The book is structured in three main parts covering all areas of parallel computing the architecture of parallel systems parallel programming models and environments and the implementation of efficient application algorithms The emphasis lies on parallel programming techniques needed for different architectures In particular this third edition includes an extended update of the chapter on computer architecture and performance analysis taking new developments such as the aspect of energy consumption into consideration The description of OpenMP has been extended and now also captures the task concept of OpenMP The chapter on message passing programming has been extended and updated to include new features of MPI such as extended reduction operations and non blocking collective communication operations The chapter on GPU programming also has been updated All other chapters also have been revised carefully The main goal of this book is to present parallel programming techniques that can be used in many situations for many application areas and to enable the reader to develop correct and efficient parallel programs Many example programs and exercises are provided to support this goal and to show how the techniques can be applied to further applications The book can be used as a textbook for students as well as a reference book for professionals The material of the book has been used for courses in parallel programming at different universities for many years

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Obaidat, 2003 Issues for 2011 cataloged as a serial in LC Computer Organization and Design David A. Patterson, John L. Hennessy, 2004-08-07 This best selling text on computer organization has been thoroughly updated to reflect the newest technologies Examples highlight the latest processor designs benchmarking standards languages and tools As with previous editions a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system The book presents an entire MIPS instruction set instruction by instruction the fundamentals of assembly language computer arithmetic pipelining memory hierarchies and I O A new aspect of the third edition is the explicit connection between program performance and CPU performance The authors show how hardware and software components such as the specific algorithm programming language compiler ISA and processor implementation impact program performance Throughout the book a new feature focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system The book digs deeper into the hardware software interface presenting a complete view of the function of the programming language and compiler crucial for understanding computer organization A CD provides a toolkit of simulators and compilers along with tutorials for using them For instructor resources click on the grey companion site button found on the right side of this page This new edition represents a major revision New to this edition Entire Text has been updated to reflect new technology 70% new exercises Includes a CD loaded with software projects and exercises to support courses using a number of tools A new interior design presents defined terms in the margin for quick reference A new feature Understanding Program Performance focuses on performance from the programmer's perspective Two sets of exercises and solutions For More Practice and In More Depth are included on the CD Check Yourself questions help students check their understanding of major concepts Computers In the Real World feature illustrates the diversity of uses for information technology More detail below

FCCM 2004 Jeffrey Arnold, Kenneth L. Pocek, 2004 FCCM presents recent work on the use of reconfigurable logic as computing elements The proceedings focuses on topics such as device architecture system architecture compilation and programming tools run time environments nano technology and applications

Digital Systems Reference Book Brian Holdsworth, Graham R. Martin, 1993 Designed to provide comprehensive coverage of the field of digital systems in a concise but authoritative form For ease of access the book has

been divided into five parts fundamentals devices for digital systems system design and techniques system development and applications **Combining Selective Cache Line Replacement and Active Management for Data Caching** Yan Ting Betty Chen,2005 **Proceedings of the First International Conference on Massively Parallel Computing Systems (MPCS), May 2-6, 1994, Ischia, Italy ,1994** The British National Bibliography Arthur James Wells,2002 **Books in Print ,1994** *ASICON 2003* Ting-Ao Tang,2003 **Conference Proceedings of the 1995 International Conference on Supercomputing ,1995**

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