Multicore Programming Guide

Multicore Programming and Applications/DSP Systems

Abstract

As application complexity continues to grow, we have reached a limit on increasing performance by merely scaling clock speed. To meet the ever-increasing processing demand, modern System-On-Chip solutions contain multiple processing cores. The dilemma is how to map applications to multicore devices. In this paper, we present a programming methodology for converting applications to run on multicore devices. We also describe the features of Texas Instruments DSPs that enable efficient implementation, execution, synchronization, and analysis of multicore applications.

Contents

Ti Multicore Programming Guide

S. Smys,Ram Palanisamy,Álvaro Rocha,Grigorios N. Beligiannis

Ti Multicore Programming Guide:

Physical Layer Multi-Core Prototyping Maxime Pelcat,Slaheddine Aridhi,Jonathan Piat,Jean-François Nezan,2012-08-11 Base stations developed according to the 3GPP Long Term Evolution LTE standard require unprecedented processing power 3GPP LTE enables data rates beyond hundreds of Mbits s by using advanced technologies necessitating a highly complex LTE physical layer The operating power of base stations is a significant cost for operators and is currently optimized using state of the art hardware solutions such as heterogeneous distributed systems The traditional system design method of porting algorithms to heterogeneous distributed systems based on test and refine methods is a manual thus time expensive task Physical Layer Multi Core Prototyping A Dataflow Based Approach provides a clear introduction to the 3GPP LTE physical layer and to dataflow based prototyping and programming The difficulties in the process of 3GPP LTE physical layer porting are outlined with particular focus on automatic partitioning and scheduling load balancing and computation latencyreduction specifically in systems based on heterogeneous multi core Digital Signal Processors Multi core prototyping methods based on algorithm dataflow modeling and architecture system level modeling are assessed with the goal of automating and optimizing algorithm porting With its analysis of physical layer processing and proposals of parallel programming methods which include automatic partitioning and scheduling Physical Layer Multi Core Prototyping A Dataflow Based Approach is a key resource for researchers and students This study of LTE algorithms which require dynamic or static assignment and dynamic or static scheduling allows readers to reassess and expand their knowledge of this vital component of LTE base station design **Multi-Core Embedded Systems** Georgios Kornaros,2018-10-08 Details a real world product that applies a cutting edge multi core architecture Increasingly demanding modern applications such as those used in telecommunications networking and real time processing of audio video and multimedia streams require multiple processors to achieve computational performance at the rate of a few giga operations per second This necessity for speed and manageable power consumption makes it likely that the next generation of embedded processing systems will include hundreds of cores while being increasingly programmable blending processors and configurable hardware in a power efficient manner Multi Core Embedded Systems presents a variety of perspectives that elucidate the technical challenges associated with such increased integration of homogeneous processors and heterogeneous multiple cores It offers an analysis that industry engineers and professionals will need to understand the physical details of both software and hardware in embedded architectures as well as their limitations and potential for future growth Discusses the available programming models spread across different abstraction levels The book begins with an overview of the evolution of multiprocessor architectures for embedded applications and discusses techniques for autonomous power management of system level parameters It addresses the use of existing open source and free tools originating from several application domains such as traffic modeling graph theory parallel computing and network simulation In addition the authors cover other important topics

associated with multi core embedded systems such as Architectures and interconnects Embedded design methodologies Mapping of applications   *Computer Networks and Inventive Communication Technologies* S. Smys,Ram Palanisamy,Álvaro Rocha,Grigorios N. Beligiannis,2021-06-02 This book is a collection of peer reviewed best selected research papers presented at 3rd International Conference on Computer Networks and Inventive Communication Technologies ICCNCT 2020 The book covers new results in theory methodology and applications of computer networks and data communications It includes original papers on computer networks network protocols and wireless networks data communication technologies and network security The proceedings of this conference is a valuable resource dealing with both the important core and the specialized issues in the areas of next generation wireless network design control and management as well as in the areas of protection assurance and trust in information security practice It is a reference for researchers instructors students scientists engineers managers and industry practitioners for advance work in the area   OpenMP in the Era of Low Power Devices and Accelerators Alistair P. Rendell,Barbara M. Chapman,Matthias S. Müller,2013-08-15 This book constitutes the refereed proceedings of the 9th International Workshop on OpenMP held in Canberra Australia in September 2013 The 14 technical full papers presented were carefully reviewed and selected from various submissions The papers are organized in topical sections on proposed extensions to OpenMP applications accelerators scheduling and tools   Manufacturing and Engineering Technology (ICMET 2014) Ai Sheng,Yizhong Wang,2014-11-24 Manufacturing and Engineering Technology brings together around 200 peer reviewed papers presented at the 2014 International Conference on Manufacturing and Engineering Technology held in San ya China October 17 19 2014 The main objective of these proceedings is to take the Manufacturing and Engineering Technology discussion a step further Contributions cover Manufacture Mechanical Materials Science Industrial Engineering Control Information and Computer Engineering Furthermore these proceedings provide a platform for researchers engineers academics as well as industrial professionals from all over the world to present their research results and development activities in Manufacturing Science and Engineering Technology   **OpenMP: Conquering the Full Hardware Spectrum** Xing Fan,Bronis R. de Supinski,Oliver Sinnen,Nasser Giacaman,2019-08-26 This book constitutes the proceedings of the 15th International Workshop on Open MP IWOMP 2019 held in Auckland New Zealand in September 2019 The 22 full papers presented in this volume were carefully reviewed and selected for inclusion in this book The papers are organized in topical sections named best paper tools accelerators compilation extensions tasking and using OpenMP   **CERN.** Institut de recherches subatomiques de Strasbourg,2008   *Electronic Design* ,1977   **Proceedings of the Technical Program, National Electronic Packaging and Production Conference** ,1985   **The Wireless World** ,1978   **Radio-electronics** ,1980   **Electronic Engineering** ,1988   **Wireless World and Radio Review** ,1974   **Government Reports Announcements & Index** ,1989-07   **InCider** ,1986   **A+.** ,1985   Insulation/circuits ,1981 Includes a special annual issue Insulation circuits directory encyclopedia   **Energy Research**

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