

Multicore Programming Guide

Multicore Programming and Applications/DSP Systems

Abstract

As application complexity continues to grow, we have reached a limit on increasing performance by merely scaling clock speed. To meet the ever-increasing processing demand, modern System-On-Chip solutions contain multiple processing cores. The dilemma is how to map applications to multicore devices. In this paper, we present a programming methodology for converting applications to run on multicore devices. We also describe the features of Texas Instruments DSPs that enable efficient implementation, execution, synchronization, and analysis of multicore applications.

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associated with multi core embedded systems such as Architectures and interconnects Embedded design methodologies Mapping of applications Computer Networks and Inventive Communication Technologies S. Smys, Ram Palanisamy, Álvaro Rocha, Grigoris N. Beligiannis, 2021-06-02 This book is a collection of peer reviewed best selected research papers presented at 3rd International Conference on Computer Networks and Inventive Communication Technologies ICCNCT 2020 The book covers new results in theory methodology and applications of computer networks and data communications It includes original papers on computer networks network protocols and wireless networks data communication technologies and network security The proceedings of this conference is a valuable resource dealing with both the important core and the specialized issues in the areas of next generation wireless network design control and management as well as in the areas of protection assurance and trust in information security practice It is a reference for researchers instructors students scientists engineers managers and industry practitioners for advance work in the area **OpenMP in the Era of Low Power Devices and Accelerators**

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