



Figure 2.1 Logic circuit for 4 bit full adder and subtractor

In summary, a 4-bit ripple carry adder cum subtractor is a versatile circuit that can handle both addition and subtraction operations on 4-bit binary numbers based on a control input. It offers flexibility in arithmetic operations while utilizing a common hardware platform.

### 2.3 PROCEDURE:

**Step 1:** Creating a Ripple Carry Adder in Verilog involves describing the behavior of the adder in a high-level hardware description language. It performs both addition and subtraction based on a control input. Here, we'll use M as the control input, where  $M = 0$  corresponds to addition, and  $M = 1$  corresponds to subtraction. Here is a simple algorithm for designing a 4-bit Ripple Carry Adder using Verilog:

#### Verilog code:

```
module Add_half (
  input a, b,
  output c_out, sum
);
  xor G1 (sum, a, b);
  and G2 (c_out, a, b);
endmodule
```

# Vtu Mechanical Design Lab Manual

**Ansel C. Ugural**



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