

- **System Tasks and Functions**
 - `vpi_register_systf()`
 - `vlog_startup_routines[]`
- **Interface**
 - **Call-backs**
 - `vpi_register_cb()`
 - **Access to HDL and Simulation Objects**
 - `module m, wire w: m1.w, m2.w`
 - **Error Handling**
 - **Function Availability**
 - **Traversing Expressions**

Vpi User Guide And Reference

Lei Huang

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Signal and Information Processing, Networking and Computers Yue Wang,Lexi Xu,Yufeng Yan,Jiaqi Zou,2020-12-17 This book collects selected papers from the 7th Conference on Signal and Information Processing Networking and Computers held in Rizhao China on September 21 23 2020 The 7th International Conference on Signal and Information Processing Networking and Computers ICSINC was held in Rizhao China on September 21 23 2020

The Verilog PLI Handbook Stuart Sutherland,2006-04-18 by Maq Mannan President and CEO DSM Technologies Chairman of the IEEE 1364 Verilog Standards Group Past Chairman of Open Verilog International One of the major strengths of the Verilog language is the Programming Language Interface PLI which allows users and Verilog application developers to infinitely extend the capabilities of the Verilog language and the Verilog simulator In fact the overwhelming success of the Verilog language can be partly attributed to the existence of its PLI Using the PLI add on products such as graphical waveform displays or pre and post simulation analysis tools can be easily developed These products can then be used with any Verilog simulator that supports the Verilog PLI This ability to create third party add on products for Verilog simulators has created new markets and provided the Verilog user base with multiple sources of software tools Hardware design engineers can and should use the Verilog PLI to customize their Verilog simulation environment A Company that designs graphics chips for example may wish to see the simulation results of a new design in some custom graphical display The Verilog PLI makes it possible and even trivial to integrate custom software such as a graphical display program into a Verilog simulator The simulation results can then dynamically be displayed in the custom format during simulation And if the company uses Verilog simulators from multiple simulator vendors this integrated graphical display will work with all the simulators

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