

## Sine Wave Generation Techniques

Producing and manipulating the sine-wave function is a common problem encountered by circuit designers. Sine-wave circuits pose a significant design challenge because they represent a consistently controlled linear oscillator. Sine-wave circuitry is required in a number of diverse areas, including audio testing, calibration equipment, transducers, power conditioning and electronic test equipment (ATE). Control of frequency, amplitude or distortion levels is often required and all three parameters must be simultaneously controlled in many applications.

A number of techniques, utilizing both analog and digital approaches are available for a variety of applications. Each individual circuit approach has inherent strengths and weaknesses which must be matched against any given application (see table).

PRIMERAS CLASES DE ORGANIZACIONES

A simple inexpensive amplitude-stabilized phase shift sine wave oscillator which requires one IC package, three transistors and runs off a single supply appears in Figure 1. Q2, in combination with the RC network, constitutes a phase

National Semiconductor  
Application Note 2903  
March 1997

shift configuration and oscillates at about 1.2 kHz. The remaining oscillator provides amplitude stability. The high impedance output of Q3's collector is fed to the input of the LM386 via the 100  $\mu$ F 100 mV/meter network. The LM386 oscillator with the internal 60 kHz unit in the LM386000 drives Q2's output by 200. This is necessary because the LM386 has a fixed gain of 200. In this manner the amplifier functions as a unity gain current buffer which will drive an HBT load. The positive peaks at the amplifier output are rectified and stored in the 5  $\mu$ F capacitor. This potential is fed to the base of Q3. Q3's collector current will vary with the difference between its base and emitter voltages. Since the emitter voltage is fixed by the LM3110 1.2V reference, Q3 performs as a comparison function and its collector current modulates Q1's base voltage. Q1, an emitter follower, provides servo-controlled drive to the Q2 oscillator. If the emitter of Q2 is opened up and driven by a control voltage, the amplitude of the circuit output may be varied. The LM386 output will drive 5W (17.75 Vrms) peak-to-peak into 8 ohm with about 2% distortion. A  $\pm 3\%$  power supply variation causes less than  $\pm 0.1$  dB amplitude shift at the output.

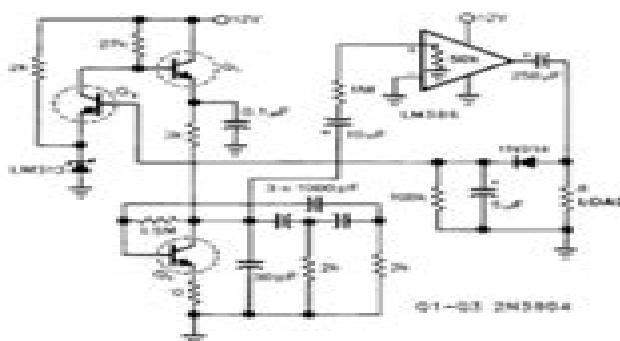


FIGURE 1. Phase-shift sine-wave oscillators combine simplicity with versatility. This 1.9-kHz design can deliver 5 Vp-p to the B11 load with about 2% distortion.

# Sine Wave Generation Techniques

**Yongsheng Gao, Shuetfung Tse, Wei  
Gao**

## **Sine Wave Generation Techniques:**

**Digital Waveform Generation** Pete Symons,2014 This overview introduces powerful flexible and practical digital waveform generation techniques enabling the design of bespoke digital waveform generation systems from scratch Including detailed hardware design examples and downloadable Mathcad models created for what if design scenarios this is essential reading for professional members of the digital signal processing community

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### **Linear Applications Handbook** National Semiconductor Corporation,1994 **Sine Wave Generator**

**Applications to Process Dynamics** J. E. Walker,W. B. Harbarger,1958 **Electronics World** ,2007 **EDN** ,1980

### **Standard Handbook for Electrical Engineers** ,1910 **Design of MOS VLSI Circuits for Telecommunications**

Yannis Tsividis,Paolo Antognetti,1985 **Measurement Technology and Intelligent Instruments VI** Yongsheng

Gao,Shuettfung Tse,Wei Gao,2005 The requirements of high precision and of high quality components and devices in meeting the needs of modern industry and society in disciplines such as semiconductors optics nanotechnology MEMS manufacturing biomedical and environmental engineering make measurement technology and intelligent instruments which sense measure and report more important than ever and essential for the rapid development of information technology

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**Measurement Technology Conference Proceedings** ,1990 *Human Brain Electrophysiology* David Regan,1989

**Technical Information Pilot** ,1952 **IC Master** ,2001 **Dr. Dobb's Journal of Software Tools for the Professional Programmer** ,1987 *14th Symposium on Integrated Circuits and Systems Design* Sociedade Brasileira de Computação,2001 Annotation Papers from a September 2001 symposium report on recent advances in areas of integrated circuits and systems design including embedded systems rapid prototyping formal methods codesign CAD and test analog digital and physical design and low power and low voltage Specific topics include communication architectures for system on chip using the CAN protocol and reconfigurable computing technology for Web based smart house automation and optimizing BBD based verification analyzing variable dependencies Other subjects include interconnection length estimation at logic level an environment to aid the synthesis of threephase analogue waveform using AHDL and extending sequencing graphs for reconfigurable applications modeling This work lacks a subject index c Book News Inc

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