

Multicore Programming Guide

Multicore Programming and Applications/DSP Systems

Abstract

As application complexity continues to grow, we have reached a limit on increasing performance by merely scaling clock speed. To meet the ever-increasing processing demand, modern System-On-Chip solutions contain multiple processing cores. The dilemma is how to map applications to multicore devices. In this paper, we present a programming methodology for converting applications to run on multicore devices. We also describe the features of Texas Instruments DSPs that enable efficient implementation, execution, synchronization, and analysis of multicore applications.

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Ti Multicore Programming Guide

Georgios Kornaros

Ti Multicore Programming Guide:

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