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Text, Speech, and Dialogue Kamil Ekštejn, František Pártl, Miloslav Konopík, 2023-08-22 This book constitutes the refereed proceedings of the 26th International Conference on Text Speech and Dialogue TSD 2023 held in Pilsen Czech Republic during September 4-6 2023. The 31 full papers presented together with the abstracts of 3 keynote talks were carefully reviewed and selected from 64 submissions. The conference attracts researchers not only from Central and Eastern Europe but also from other parts of the world. One of its goals has always been bringing together NLP researchers with various interests from different parts of the world and promoting their cooperation. One of the ambitions of the conference is not only to deal with dialogue systems but also to improve dialogue among researchers in areas of NLP i.e. among the text and the speech and the dialogue people

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[], Aster, 2024-10-18 NeRF NeRF NeRF NeRF NeRF 1 NeRF 3DGS 2 Luma AI NeRF 3 4 NeRF 3DGS 5 Nerfstudio 6 Volinga

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Harnessing NVIDIA GPUs Potential Brian Pitman, 2025-01-23

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Collective Communication and Barrier Synchronization on NVIDIA GPU Diego Alejandro Rivera-Polanco,2009

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General-Purpose Graphics Processor Architectures Tor M.

Aamodt,Wilson Wai Lun Fung,Timothy G. Rogers,2018-05-21 Originally developed to support video games graphics processor units GPUs are now increasingly used for general purpose non graphics applications ranging from machine learning to mining of cryptographic currencies GPUs can achieve improved performance and efficiency versus central processing units CPUs by dedicating a larger fraction of hardware resources to computation In addition their general purpose programmability makes contemporary GPUs appealing to software developers in comparison to domain specific accelerators This book provides an introduction to those interested in studying the architecture of GPUs that support general purpose computing It collects together information currently only found among a wide range of disparate sources The authors led development of the GPGPU Sim simulator widely used in academic research on GPU architectures The first chapter of this book describes the basic hardware structure of GPUs and provides a brief overview of their history Chapter 2 provides a summary of GPU programming models relevant to the rest of the book Chapter 3 explores the architecture of GPU compute cores Chapter 4 explores the architecture of the GPU memory system After describing the architecture of existing systems Chapters 3 and 4 provide an overview of related research Chapter 5 summarizes cross cutting research impacting both the compute core and memory system This book should provide a valuable resource for those wishing to understand the architecture of graphics processor units GPUs used for acceleration of general purpose applications and to those who want to obtain an introduction to the rapidly growing body of research exploring how to improve the architecture of these GPUs

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