SYNTHESIS & OPTIMIZATION OF DIGITAL CIRCUITS

Synthesis And Optimization Of Digital Circuits

Michael Hoelscher

Synthesis And Optimization Of Digital Circuits:

    <u>Synthesis and Optimization of Digital Circuits</u> Giovanni De Micheli,1994 The book provides a thorough explanation of synthesis and optimization algorithms accompanied by a sound mathematical formulation and a unified notation

    Synthesis and Optimization of FPGA-Based Systems Valery Sklyarov,Iouliia Skliarova,Alexander Barkalov,Larysa Titarenko,2014-03-14 The book is composed of two parts The first part introduces the concepts of the design of digital systems using contemporary field programmable gate arrays FPGAs Various design techniques are discussed and illustrated by examples The operation and effectiveness of these techniques is demonstrated through experiments that use relatively cheap prototyping boards that are widely available The book begins with easily understandable introductory sections continues with commonly used digital circuits and then gradually extends to more advanced topics The advanced topics include novel techniques where parallelism is applied extensively These techniques involve not only core reconfigurable logical elements but also use embedded blocks such as memories and digital signal processing slices and interactions with general purpose and application specific computing systems Fully synthesizable specifications are provided in a hardware description language VHDL and are ready to be tested and incorporated in engineering designs A number of practical applications are discussed from areas such as data processing and vector based computations e g Hamming weight counters comparators The second part of the book covers the more theoretical aspects of finite state machine synthesis with the main objective of reducing basic FPGA resources minimizing delays and achieving greater optimization of circuits and systems

    Constrained Synthesis and Optimization of Digital Integrated Circuits from Behavioral Specifications David Cho-Lun Ku,Stanford University. Computer Systems Laboratory,1991   **Logic Synthesis and Optimization** Tsutomu Sasao,2012-12-06 Logic Synthesis and Optimization presents up to date research information in a pedagogical form The authors are recognized as the leading experts on the subject The focus of the book is on logic minimization and includes such topics as two level minimization multi level minimization application of binary decision diagrams delay optimization asynchronous circuits spectral method for logic design field programmable gate array FPGA design EXOR logic synthesis and technology mapping Examples and illustrations are included so that each contribution can be read independently Logic Synthesis and Optimization is an indispensable reference for academic researchers as well as professional CAD engineers

    <u>IEEE Circuits & Devices</u> ,1996   <u>Representing and Modeling Digital Circuits</u> Tomas Gerhard Rokicki,1993

    <u>Readings in Hardware/Software Co-Design</u> Giovanni De Micheli,Rolf Ernst,Wayne Wolf,2002 This title serves as an introduction ans reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s   **Spectral Logic and Its Applications for the Design of Digital Devices** Mark G. Karpovsky,Radomir S. Stankovic,Jaakko T. Astola,2008-05-27 Spectral techniques facilitate the design and testing of today s increasingly complex digital devices There is heightened interest in spectral techniques for the design of digital devices dictated by ever increasing

demands on technology that often cannot be met by classical approaches Spectral methods provide a uniform and consistent theoretic environment for recent achievements in this area which appear divergent in many other approaches Spectral Logic and Its Applications for the Design of Digital Devices gives readers a foundation for further exploration of abstract harmonic analysis over finite groups in the analysis design and testing of digital devices After an introduction this book provides the essential mathematical background for discussing spectral methods It then delves into spectral logic and its applications covering Walsh Haar arithmetic transform Reed Muller transform for binary valued functions and Vilenkin Chrestenson transform generalized Haar and other related transforms for multiple valued functions Polynomial expressions and decision diagram representations for switching and multiple value functions Spectral analysis of Boolean functions Spectral synthesis and optimization of combinational and sequential devices Spectral methods in analysis and synthesis of reliable devices Spectral techniques for testing computer hardware This is the authoritative reference for computer science and engineering professionals and researchers with an interest in spectral methods of representing discrete functions and related applications in the design and testing of digital devices It is also an excellent text for graduate students in courses covering spectral logic and its applications *Functional Design Errors in Digital Circuits* Kai-hui Chang,Igor L. Markov,Valeria Bertacco,2008-12-02 Functional Design Errors in Digital Circuits Diagnosis covers a wide spectrum of innovative methods to automate the debugging process throughout the design flow from Register Transfer Level RTL all the way to the silicon die In particular this book describes 1 techniques for bug trace minimization that simplify debugging 2 an RTL error diagnosis method that identifies the root cause of errors directly 3 a counterexample guided error repair framework to automatically fix errors in gate level and RTL designs 4 a symmetry based rewiring technology for fixing electrical errors 5 an incremental verification system for physical synthesis and 6 an integrated framework for post silicon debugging and layout repair The solutions provided in this book can greatly reduce debugging effort enhance design quality and ultimately enable the design and manufacture of more reliable electronic devices **Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology** ,1997 **Multi-Threshold CMOS Digital Circuits** Mohab Anis,Mohamed Elmasry,2012-12-06 Multi Threshold CMOS Digital Circuits Managing Leakage Power discusses the Multi threshold voltage CMOS MTCMOS technology that has emerged as an increasingly popular technique to control the escalating leakage power while maintaining high performance The book addresses the leakage problem in a number of designs for combinational sequential dynamic and current steering logic Moreover computer aided design methodologies for designing low leakage integrated circuits are presented The book give an excellent survey of state of the art techniques presented in the literature as well as proposed designs that minimize leakage power while achieving high performance Multi Threshold CMOS Digital Circuits Managing Leakage Power is written for students of VLSI design as well as practicing circuit designers system designers CAD tool developers and researchers It assumes a basic knowledge of digital circuit design and device operation and covers a broad

range of circuit design techniques **Proceedings of the ... Midwest Symposium on Circuits and Systems** ,1999 Logic Synthesis for FSM-Based Control Units Alexander Barkalov,Larysa Titarenko,2009-11-25 This book presents the hardware implementation of control algorithms represented by graph schemes of algorithm It includes new methods of logic synthesis and optimization for logic circuits of Mealy and Moore FSMs oriented on both ASIC and FPLD **VLSI Design 2001 : Fourteenth International Conference on VLSI Design** VLSI Society of India,2001 The International Conference on VLSI Design was started in 1985 as a workshop and from this start has grown into an international conference on VLSI design The proceedings are dedicated to all aspects of integrated circuit design technology and related computer aided design CAD Nanoelectronic Mixed-Signal System Design Saraju Mohanty,2015-02-20 Covering both the classical and emerging nanoelectronic technologies being used in mixed signal design this book addresses digital analog and memory components Winner of the Association of American Publishers 2016 PROSE Award in the Textbook Physical Sciences Mathematics category Nanoelectronic Mixed Signal System Design offers professionals and students a unified perspective on the science engineering and technology behind nanoelectronics system design Written by the director of the NanoSystem Design Laboratory at the University of North Texas this comprehensive guide provides a large scale picture of the design and manufacturing aspects of nanoelectronic based systems It features dual coverage of mixed signal circuit and system design rather than just digital or analog only Key topics such as process variations power dissipation and security aspects of electronic system design are discussed Top down analysis of all stages from design to manufacturing Coverage of current and developing nanoelectronic technologies not just nano CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability Advances in Machine Learning Research and Application: 2012 Edition ,2012-12-26 Advances in Machine Learning Research and Application 2012 Edition is a ScholarlyEditions eBook that delivers timely authoritative and comprehensive information about Machine Learning The editors have built Advances in Machine Learning Research and Application 2012 Edition on the vast information databases of ScholarlyNews You can expect the information about Machine Learning in this eBook to be deeper than what you can access anywhere else as well as consistently reliable authoritative informed and relevant The content of Advances in Machine Learning Research and Application 2012 Edition has been produced by the world s leading scientists engineers analysts research institutions and companies All of the content is from peer reviewed sources and all of it is written assembled and edited by the editors at ScholarlyEditions and available exclusively from us You now have a source you can cite with authority confidence and credibility More information is available at http www ScholarlyEditions com **Proceedings** ,2002 *Synthesis and Optimization of DSP Algorithms* George Constantinides,Peter Y.K. Cheung,Wayne Luk,2007-05-08 Synthesis and Optimization of DSP Algorithms describes approaches taken to synthesising structural hardware descriptions of digital circuits from high level descriptions of Digital

Signal Processing DSP algorithms The book contains A tutorial on the subjects of digital design and architectural synthesis intended for DSP engineers A tutorial on the subject of DSP intended for digital designers A discussion of techniques for estimating the peak values likely to occur in a DSP system thus enabling an appropriate signal scaling Analytic techniques simulation techniques and hybrids are discussed The applicability of different analytic approaches to different types of DSP design is covered The development of techniques to optimise the precision requirements of a DSP algorithm aiming for efficient implementation in a custom parallel processor The idea is to trade off numerical accuracy for area or power consumption advantages Again both analytic and simulation techniques for estimating numerical accuracy are described and contrasted Optimum and heuristic approaches to precision optimisation are discussed A discussion of the importance of the scheduling allocation and binding problems and development of techniques to automate these processes with reference to a precision optimized algorithm Future perspectives for synthesis and optimization of DSP algorithms **Digest of Technical Papers** ,1996 **Proceedings of Euro ASIC '90** Institut national polytechnique (Grenoble). Club conception de circuits à la demande,1990

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