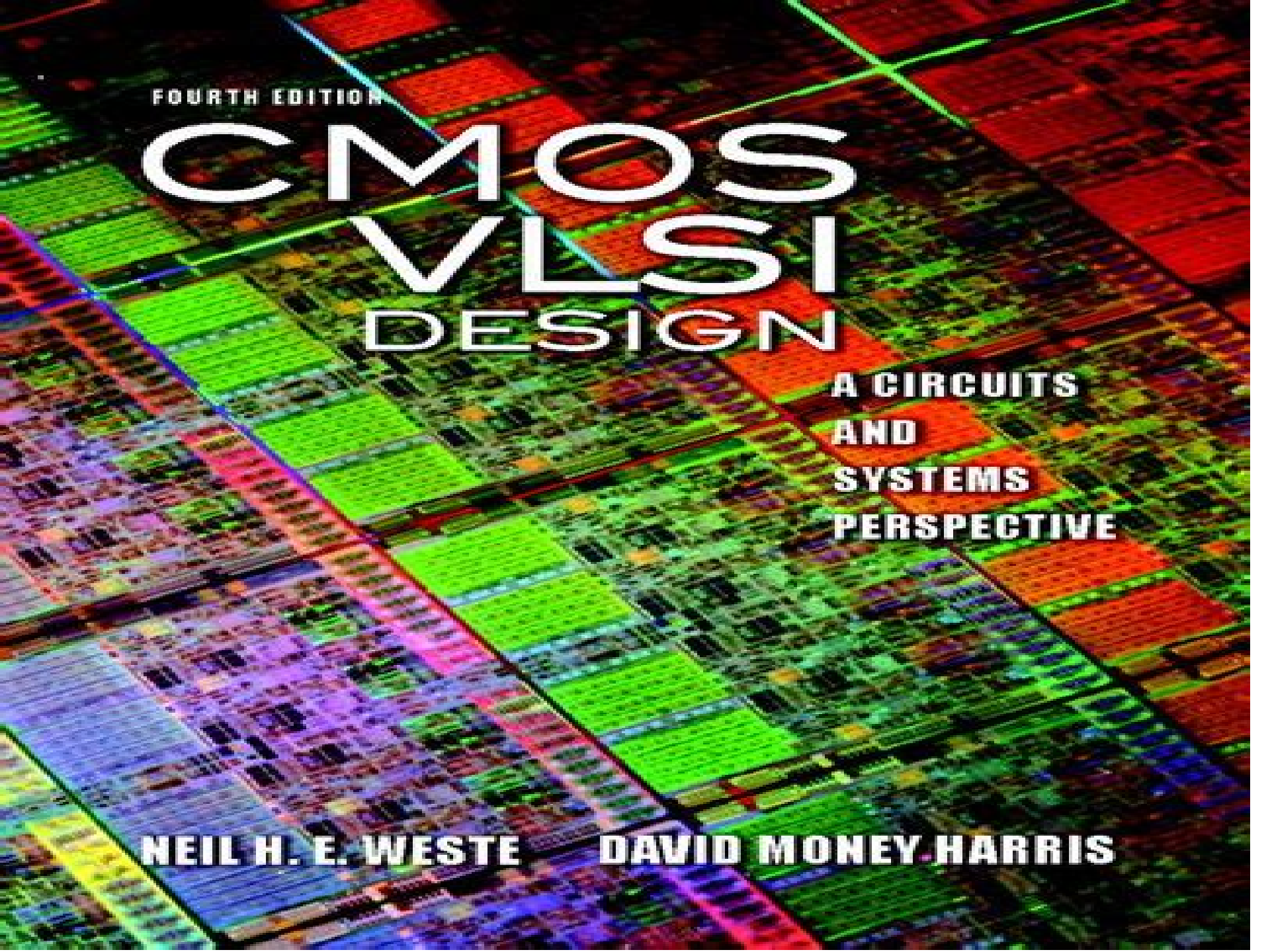


The background of the book cover is a high-resolution, false-color micrograph of a silicon microchip die. The die is rectangular and densely packed with intricate circuit patterns. Various regions are highlighted in different colors: large rectangular blocks of red, green, and blue, and a complex network of fine lines and smaller structures in purple, yellow, and black. The overall effect is a detailed, technical representation of modern semiconductor technology.

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Principles of CMOS VLSI Design Neil H. E. Weste,Kamran Eshraghian,1993 With this revision Weste conveys an understanding of CMOS technology circuit design layout and system design sufficient to the designer The book deals with the technology down to the layout level of detail thereby providing a bridge from a circuit to a form that may be fabricated

Solutions Manual for Low-Voltage Cmos Vlsi Circuits Kuo,1999-01-01 Digital Vlsi Design Singh Ajay Kumar,2010-06-30 This well organised book provides an in depth coverage of VLSI design engineering which ranges from CMOS logic to physical design automation The book begins with a discussion on the structure and operation of MOS as MOSFET is the basic building block for any VLSI design Then it goes on to explain the various fabrication methods of MOSFET and CMOS implementation and properties of MOS inverter circuit and parasitic parameters and resistances associated with MOSFET which determine and ultimately limit the performance of a digital system Besides it describes design methodology and the concept of the combinational static logic circuits sequential circuit design and CMOS dynamic circuits Finally the book examines semiconductor memory and the importance of adder and multiplier circuits for the VLSI designer Primarily intended as a text for the undergraduate and postgraduate students of Electrical and Electronics Engineering the book would also be of considerable value to designers both beginners and professionals Key Features Provides mathematical derivations for both noise margin and logic voltage Explains all combinational and sequential logics separately Contains a large number of solved and unsolved problems based on issues related to digital VLSI design

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