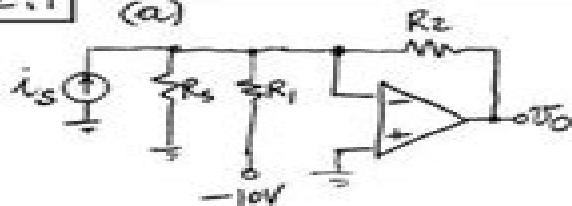


CH. 2 – PROBLEM SOLUTIONS

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2.1 (a)



$$R_2 = \frac{10V_0}{\Delta i_s} = \frac{5 - (-5)}{(1-0) \text{ mA}} = 10 \text{ k}\Omega .$$

$$i_s = 0 \Rightarrow V_0 = +5V = -\frac{R_2}{R_1}(-10) \\ \Rightarrow R_1 = 2R_2 = 20 \text{ k}\Omega .$$

$$(b) \beta = \frac{R_2/R_1}{R_2/(R_1+R_2)} . R_2 = 0 \Rightarrow \beta = \frac{20}{20+10} = \frac{2}{3} , T = \alpha \beta = \frac{2}{3} \mu .$$

$$0.01\% \text{ max deviation} \Rightarrow \frac{1}{T} \leq \frac{0.01}{100} \Rightarrow T > 10^4 \Rightarrow \frac{2}{3} \alpha > 10^4 \Rightarrow \alpha > 15,000 \text{ V/V} .$$

(c) 0.025% max deviation with $\alpha = 15,000 \text{ V/V}$ and R_s in place

$$\Rightarrow \frac{1}{15,000 \beta} \leq \frac{0.025}{100} \Rightarrow \beta \geq 3.75 \Rightarrow \frac{1}{1+R_2/R_s + R_2/R_1} \geq \frac{1}{3.75} \Rightarrow$$

$$\frac{1}{1+10/R_s + 0.5} \geq \frac{1}{3.75} \Rightarrow \frac{10}{R_s} \leq 2.25 \Rightarrow R_s \geq 4.444 \text{ k}\Omega .$$

Solution Manual For Design With Operational Amplifiers

Yijin Wang

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Solution Manual Operational Amplifier Circuits David E. Johnson,V. Jayakumar,1982 **Numerical and Evolutionary Optimization - NEO 2017** Leonardo Trujillo,Oliver Schütze,Yazmin Maldonado,Paul Valle,2018-07-12 This book features 15 chapters based on the Numerical and Evolutionary Optimization NEO 2017 workshop held from September 27 to 29 in the city of Tijuana Mexico The event gathered researchers from two complimentary fields to discuss the theory development and application of state of the art techniques to address search and optimization problems The lively event included 7 invited talks and 64 regular talks covering a wide range of topics from evolutionary computer vision and machine learning with evolutionary computation to set oriented numeric and steepest descent techniques Including research submitted by the NEO community the book provides informative and stimulating material for future research in the field **Photodiode**

Amplifiers: OP AMP Solutions Jerald G. Graeme,1996 Light photons impinging upon a semiconductor material in the vicinity of a P N junction release conduction carriers to produce current flow through the photodiode effect Photodiode amplifiers convert this current to a voltage in a relationship that remains linear as long as the amplifier eliminates signal voltage swing from the photodiode For this purpose the simple current to voltage converter or transimpedance amplifier presents a virtual ground to the diode However when connected to a photodiode this simple op amp circuit displays surprising multidimensional constraints that defy conventional op amp intuition *Scientific and Technical Books and Serials in Print ,1989* **Basic Engineering Circuit Analysis** J. David Irwin,R. Mark Nelms,2020-08-18 Basic Engineering Circuit Analysis has long been regarded as the most dependable textbook for computer and electrical engineering majors In this new edition Irwin and Nelms continue to develop the most complete set of pedagogical tools available and provide the highest level of support for students entering into this complex subject Irwin and Nelms trademark student centered learning design focuses on helping students complete the connection between theory and practice Key concepts are explained clearly and illustrated by detailed worked examples These are then followed by Learning Assessments which allow students to work similar problems and check their results against the answers provided **CMOS MOS** (CMOS—Digital Integrated Circuits) Behzad Razavi,2005 CMOS MOS *Subject Guide to Books in Print ,1993* **Electronic Circuit Analysis and Design** Donald A. Neamen,2001 This junior level electronics text provides a foundation for analyzing and designing analog and digital electronic circuits Computer analysis and design are recognized as significant factors in electronics throughout the book The use of computer tools is presented carefully alongside the important hand analysis and calculations The author Don Neamen has many years experience as an engineering educator and an engineer His experience shines through each chapter of the book rich with realistic examples and practical rules of thumb The book is divided into three parts Part 1 covers semiconductor devices and basic circuit applications Part 2 covers more advanced topics in analog electronics and Part 3 considers digital electronic circuits *Computer-Aided Design of Analog Integrated Circuits and Systems* Rob A.

Rutenbar,Georges G. E. Gielen,2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today's new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck [Books in Print Supplement](#) ,2002 [**Books in Print**](#) ,1981 [Books in Series](#) ,1985 Vols for 1980 issued in three parts Series Authors and Titles [**International Journal of Electrical Engineering Education**](#) ,1989

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